Transistors

2.5V Drive Nch MOS FET RJU002N06

Structure

Silicon N-channel MOS FET

Features

- 1) Low On-resistance.
- 2) Low voltage drive (2.5V drive).

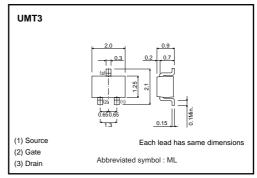
Applications

Switching

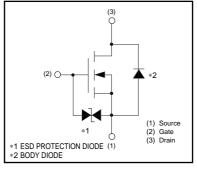
Packaging specifications

	Package	Taping	
Туре	Code	T106	
	Basic ordering unit (pieces)	3000	
RJU002N06	0		

•External dimensions (Unit : mm)



Inner circuit



●Absolute maximum ratings (Ta=25°C)

Parameter		Symbol	Limits	Unit
Drain-source voltage		VDSS	60	V
Gate-source voltage		V _{GSS}	±12	V
Ducia como at	Continuous	ID	±200	mA
Drain current	Pulsed	IDP *1	±800	mA
Total power dissipation		P _D *2	200	mW
Channel temperature		Tch	150	°C
Range of storage temperature		Tstg	-55 to +150	°C

*1 Pw≤10µs, Duty cycle≤1%
*2 Each terminal mounted on a recommended land

Thermal resistance

Parameter	Symbol	Limits	Unit	
Channel to ambient	Rth(ch-a)*	625	°C/W	

* Each terminal mounted on a recommended land

Transistors

•Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions	
Gate-source leakage	lgss	-	-	±10	μΑ	Vgs=±12V, Vds=0V	
Drain-source breakdown voltage	V(BR) DSS	60	-	-	V	I _D = 1mA, V _{GS} =0V	
Zero gate voltage drain current	IDSS	-	-	1	μΑ	V _{DS} = 60V, V _{GS} =0V	
Gate threshold voltage	VGS (th)	0.5	-	1.5	V	V _{DS} = 10V, I _D = 1mA	
Static drain-source on-state resistance	RDS (on)*	-	1.6	2.3	Ω	I _D = 200mA, V _{GS} = 4.5V	
		-	1.7	2.4	Ω	I _D = 200mA, V _{GS} = 4V	
		-	2.2	3.1	Ω	I _D = 200mA, V _{GS} = 2.5V	
Forward transfer admittance	Y _{fs} *	0.1	-	-	S	V _{DS} = 10V, I _D = 200mA	
Input capacitance	Ciss	-	18	-	pF	VDs= 10V	
Output capacitance	Coss	-	7	-	pF	Vgs=0V	
Reverse transfer capacitance	Crss	-	5	-	рF	f=1MHz	
Turn-on delay time	t _{d (on)} *	-	7	-	ns	Vdd≒ 30V	
Rise time	tr *	_	7	-	ns	ID= 100mA VGS= 4V	
Turn-off delay time	td (off) *	_	12	-	ns	VGS= 4V RL=300Ω	
Fall time	t _f *	_	90	-	ns	R _G =10Ω	

•Body diode characteristics (Source-drain) (Ta=25°C)

Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Forward voltage	Vsd	-	-	1.2	V	Is= 0.16A, V _{GS} =0V

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